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ART/Ada Design Project - Phase I Project Plan

Status Report

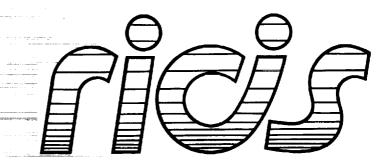
Bradley P. Allen

Inference Corporation

October 24, 1988

Cooperative Agreement NCC 9-16 Research Activity No. SE.19

NASA Johnson Space Center Information Systems Directorate Information Technology Division



Research Institute for Computing and Information Systems University of Houston - Clear Lake

N92-11663

PLAN Status Report,

ART/Ada DESIGN PROJECT

The RICIS Concept

The University of Houston-Clear Lake established the Research Institute for Computing and Information systems in 1986 to encourage NASA Johnson Space Center and local industry to actively support research in the computing and information sciences. As part of this endeavor, UH-Clear Lake proposed a partnership with JSC to jointly define and manage an integrated program of research in advanced data processing technology needed for JSC's main missions, including administrative, engineering and science responsibilities. JSC agreed and entered into a three-year cooperative agreement with UH-Clear Lake beginning in May, 1986, to jointly plan and execute such research through RICIS. Additionally, under Cooperative Agreement NCC 9-16, computing and educational facilities are shared by the two institutions to conduct the research.

The mission of RICIS is to conduct, coordinate and disseminate research on computing and information systems among researchers, sponsors and users from UH-Clear Lake, NASA/JSC, and other research organizations. Within UH-Clear Lake, the mission is being implemented through interdisciplinary involvement of faculty and students from each of the four schools: Business, Education, Human Sciences and Humanities, and Natural and Applied Sciences.

Other research organizations are involved via the "gateway" concept. UH-Clear Lake establishes relationships with other universities and research organizations, having common research interests, to provide additional sources of expertise to conduct needed research.

A major role of RICIS is to find the best match of sponsors, researchers and research objectives to advance knowledge in the computing and information sciences. Working jointly with NASA/JSC, RICIS advises on research needs, recommends principals for conducting the research, provides technical and administrative support to coordinate the research, and integrates technical results into the cooperative goals of UH-Clear Lake and NASA/JSC.

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Preface

This research was conducted under auspices of the Research Institute for Computing and Information Systems by Inference Corporation. Dr. Charles McKay served as RICIS research coordinator.

Funding has been provided by the Information Systems Directorate, NASA/JSC through Cooperative Agreement NCC 9-16 between the NASA Johnson Space Center and the University of Houston-Clear Lake. The NASA technical monitor for this activity was Robert T. Savely, of the Software Technology Branch, Information Technology Division, Information Systems Directorate, NASA/JSC.

The views and conclusions contained in this report are those of the author and should not be interpreted as representative of the official policies, either express or implied, of NASA or the United States Government.

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Subcontract 015

RICIS Research Activity SE.19

NASA Cooperative Agreement NCC-9-16

March 1988 - October 1988

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24 October 88 16:42

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1. Project Plan and Schedule

1.1 Introduction

This chapter describes the plan and schedule for Phase I of the Ada-Based ESBT Design Research Project.

1.2 Platform

The main platform for the project is a DEC Ada compiler on VAX mini-computers and VAX stations running the VMS operating system.

1.3 Resource Requirement

The project requires the following resources:

- One principal investigator: Bradley P. Allen (BPA)
- Two researchers: James T. Badura (JTB) and S. Daniel Lee (SDL)
- Hardware: one VAXstation II, one Sun 3/260, and one IBM PS/2 Model 50 or 70.
- Ada compilers: one copy of the DEC Ada compiler for a VAXstation, one copy of the Verdix Ada compiler for a Sun, and one copy of the Alsys Ada compiler for an IBM PS/2
- C compiler: one copy of the VAX C compiler for a VAXstation, and one copy of the Microsoft C compiler for an IBM PS/2
- ART-IM Version 1.5
- ART/Ada Test cases: an extensive test suite similar to the ART-IM test suit, a benchmark program, and other small test cases.

1.4 Effort

The individual effort and the lines of code (LOC) for the project is shown in the following table. If the status is marked as "DONE", the effort and LOC are the actual numbers. Otherwise, they are estimated.

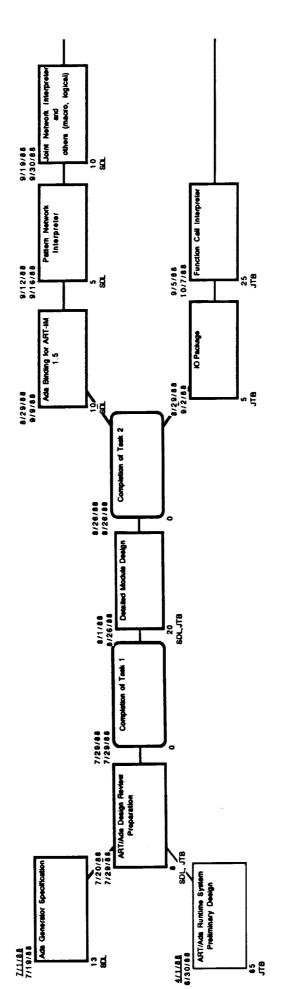
ART/Ada Effort and Lines-of-Code(LOC)

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Task	Package Name	Man-Hours	urs Man-Weeks	3	LOCAVEEK	LOC/ PERSON		STATUS
						SDL	JTB	
Detailed Module Design			160					DONE
Detailed Module Design			120					BONE
Ada Binding for ART-IM 1.5	art		80	218	1094	2187		BONE
IO Package	io sub		04	503	503){	2	503 DONE
Pattern Network Interpreter pattern net sub	pattern net sub		40	409	409	409		BOK BOK
Join Network Interpreter	join net sub		6.4 1.6	622		622		BONE E
Macro Package	macro sub		8 0.2	380	1900	380		BOE BOE
Logical Dependency Package	logical sub (just a stub)		8 0.3	172	860	172		BONE
Database Manager	database sub		24 0.6	806	1343	908		BONE
Call-in Package	callio sub		0.0	209	2090	209		BONE
Memory Allocation Package	alloc sub		4 0.1	350	3500	350		SONE BONE
Agenda Manager	agenda sub		8 0.2	191	955	191		8 8
Inference Engine	infer eng sub, init sub		40	870	870		8	870 DONE
Function Call Interpreter	interpreter sub		256 6.4	896	151	768		700 DONE
ART Object Manager	art object sub		56 1.4	009	429		9	009
ART Object Utilities	art object util sub		16	400	1000		*	400
Error Handler	error hdl sub		8 0.2	200	1000		2	200
Compiler Specific Package	compiler dcl		8 0.2	50	250			5.0
Ul internal Package	ui internal sub		16 0.4	200		~	5(200
Math Package	math sub		16 0.4	300	750		3	300
User Interface	lex ado, parser ado, user inte	erface sub	40	500	200		9	500
Code Vector Conversion Design	u l		24 0.6					
Integrated Testing I			80 2					
Ada Generator			200	5000	1000	2000		
Integrated Testing II			400					
Total			1720 43	14917	347	11094	4323	23

1.5 Schedule

The PERT chart for the entire project life cycle is shown in the following pages:



SDL = S. Daniel Lee JTB = J. T. Bedura

